

Customer No. 22,852 Attorney Docket No. 04284.0856

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:	)
Atsushi SHIMBO	)
Serial No.: 10/051,280	) Group Art Unit: 2121 )
Filed: January 22, 2002	) Examiner: Not Yet Assigned )
For: MODULAR ARITHMETIC APPARATUS AND METHOD SELECTING A BASE IN A RESIDUE NUMBER SYSTEM	) ) )

Assistant Commissioner for Patents Washington, DC 20231

**BOX: PATENT APPLICATION** 

Sir:

## INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97(b)

Pursuant to 37 C.F.R. §§1.56 and 1.97(b), applicant brings to the Examiner's attention the documents listed on attached Form PTO-1449. Copies of the listed documents are attached. Applicant respectfully requests that the Examiner consider the documents listed on attached Form PTO-1449 and indicate that they were considered by making an appropriate notation on this form.

This Information Disclosure Statement is being filed with the above-referenced application.

This submission does not represent that a search has been made or that no better art exists and does not constitute an admission that each or all of the listed documents are material or constitute "prior art." If the Examiner applies any of the

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documents as prior art against any claim in the application and applicant determines that the cited documents do not constitute "prior art" under United States law, applicant reserves the right to present to the office the relevant facts and law regarding the appropriate status of such documents. Applicant further reserves the right to take appropriate action to establish the patentability of the disclosed invention over the listed documents, should one or more of the documents be applied against the claims of the present application.

If there is any fee due in connection with the filing of this Statement, please charge the fee to our Deposit Account No. 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW, GARRETT & DUNNER, L.L.P.

Dated: March 22, 2002

Richard W. Burgujian Reg. No. 81,744

Enclosures RVB/FPD/sci

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## INFORMATION DISCLOSURE CITATION

Atty. Docket No.	04284.0856	Serial No.	10/051,280	
Applicant	Atsushi SHIMBO	MAR 2 2 2002 00		
Filing Date	January 22, 2002	Group:	Not Yet Assigned	
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U.S. PATENT DOCUMENTS						
Examiner Initial*	Document Number	Issue Date	Name	Class	Sub Class	Filing Date If Appropriate

FOREIGN PATENT DOCUMÉNTS						
	Document Number	Publication Date	Country	Class	Sub Class	Translation Yes or No
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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)				
	S. KAWAMURA et al., "COX-ROWER ARCHITECTURE FOR FAST PARALLEL MONTGOMERY MULTIPLICATION", Eurocrypt 2000, LNCS 1807, pp. 523-538, 2000.			
	K. POSCH et al., "MODULO REDUCTION IN RESIDUE NUMBER SYSTEMS", IEEE Transactions on Parallel and Distributed Systems, Vol. 6, No. 5, pp. 449-454, May 1995.			

Examiner		Date Considered			
*Examiner:		I if reference considered, whether or not citation is in conformance with MPEP 609; draw line igh citation if not in conformance and not considered. Include copy of this form with next munication to applicant.			
Form PTO 144	Patent and	Trademark Office - U.S. Department of Commerce			